

PHYS 235

Applied Electronics

Spring 2009

Instructor: **Marty Ligare**

Office: Olin 167

Lab: Olin 156

Phone: 7-1213 or 7-1207

email: mligare@bucknell.edu

course website: <http://www.eg.bucknell.edu/physics/ph235/>

Office Hours (tentative): MWF 9-11, 3-4, TR 3-4

Synopsis

This course will introduce you to both analog and digital electronics, as well as brief introduction of interfacing of lab equipment to computers. The heart of this course is the lab-work you will do; there is one lecture for every lab, and in general the lectures will be specifically designed to give you the background necessary for the upcoming lab session. The topics will include the following:

- Topic 1: Analog Electronics: Circuits with linear, passive elements; Kirchoff's Laws, DC circuits, AC circuits (RC filters, resonance), equivalent circuits.
- Topic 2: Analog Electronics: Circuits with nonlinear, active elements; Diodes, transistors, op-amps, feedback.
- Topic 3: Digital Electronics: Logic gates, timers, counters, shift registers.
- Topic 4: Analog-to-Digital Conversion/Digital-to-Analog Conversion
- Topic 5: Computer Interfacing.
- Topic 6: Return to Analog (while you are working on projects): Fourier analysis; Noise/Interference.

Texts

The only required text for the course is the *PHYS 235 Applied Electronics Laboratory Manual* that is available in the bookstore. Copies of the following supplementary texts will be available in the lab, and I encourage you to take a look at them. If you find any of them particularly useful you should purchase your own copy.

- *Introductory Electronics for Scientists and Engineers*, R. E. Simpson, 2nd ed., (Allyn and Bacon, 1987)
- *The Art of Electronics*, P. Horowitz and W. Hill, 2nd ed., (Cambridge Univ. Press, 1989)
- *Schaum's Outline of Basic Circuit Analysis*, J. O'Malley, 2nd ed., (McGraw-Hill, 1992)
- *Schaum's Outline of Electric Circuits*, M. Nahvi and J. Edminster, 4th ed., (McGraw-Hill, 2002)
- *Schaum's Outline of Digital Principles*, R. Tokheim, 3rd ed., (McGraw-Hill, 1994)
- *Practical Electronics for Inventors*, 2nd ed., Paul Scherz (McGraw-Hill, 2007)

Labs

Because the labs are the heart of the course, completion of all lab exercises is mandatory. Your lab book will be graded after every lab.

Homework

I will assign problems to accompany each lecture/lab. Once a week I will collect selected problems from this list for grading. Although I will not be grading every problem, you are responsible for the material covered in all of the problems. I encourage collaboration in working on homework, but all submitted problems must represent your own articulation of the work and your understanding.

Exams

There will be three in-class exams and a comprehensive final. The tentative in-class exam dates are:

- Tuesday February 10,
- Thursday March 5 (last class before spring break), and
- Tuesday April 14.

Final Project

During the last three weeks of the semester (or so) you will work on a comprehensive project that should integrate many aspects of the course. You can choose a project from a list of suggestions, or you can come up with your own idea. You will design the circuit, build and troubleshoot it, and demonstrate that it performs the intended function. You will also write a “manual” that gives operating instructions, schematic diagrams, and a discussion of design principles.

Grading

- Final Project: 15%
- Lab Work: 20%
- Homework: 15%
- In-class Exams: 30%
- Final Exam: 20%

Bucknell Honor Code

As a student and citizen of the Bucknell University community:

1. I will not lie, cheat, or steal in my academic endeavors.
2. I will forthrightly oppose each and every instance of academic dishonesty.
3. I will let my conscience guide my decision to communicate directly with any person or persons I believe to have been dishonest in academic work.
4. I will let my conscience guide my decision on reporting breaches of academic integrity to the appropriate faculty or deans.