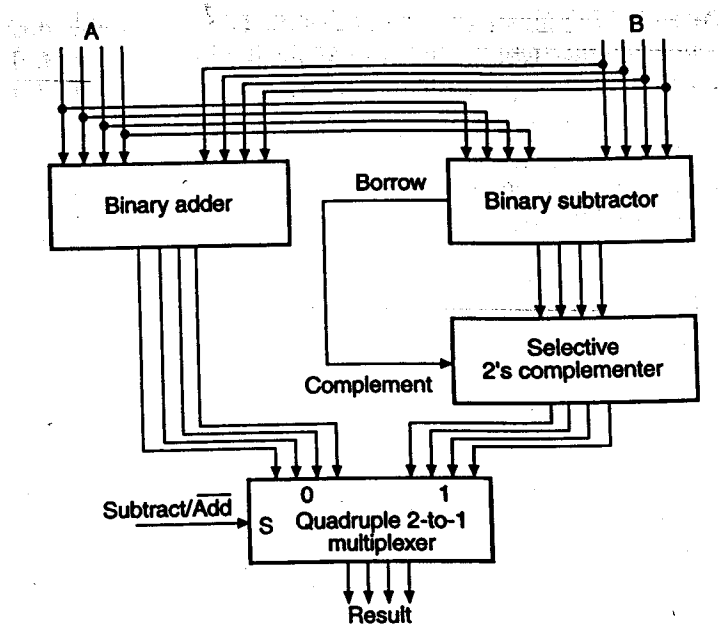
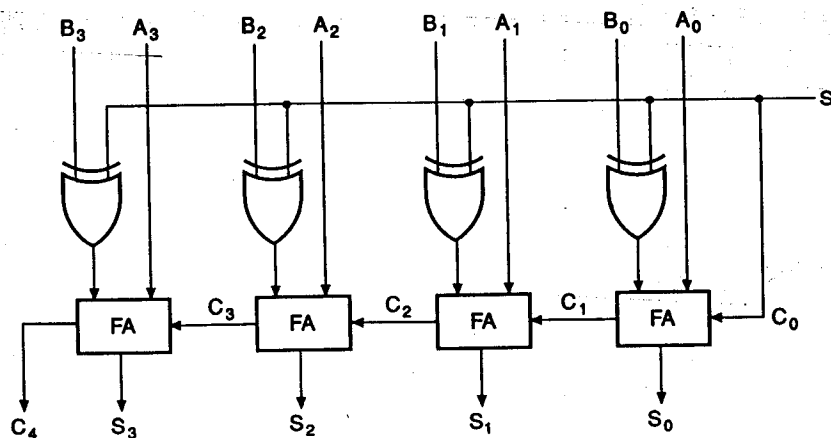




□ **FIGURE 3-30**
Block Diagram of Binary Adder-Subtractor



□ **FIGURE 3-31**
Adder-Subtractor Circuit