

Sequential Logic Circuit Design

For design, we need to know which inputs should be applied to a flip-flop to change its "state".

⇒ Excitation Table

SR :

Present State $Q(t)$	Next State $Q(t+1)$	Inputs	
		S	R
0	0		
0	1		
1	0		
1	1		

D :

Present State $Q(t)$	Next State $Q(t+1)$	<u>D</u>
0	0	
0	1	
1	0	
1	1	

JK :

Present State $Q(t)$	Next State $Q(t+1)$	<u>J</u>	<u>K</u>
0	0		
0	1		
1	0		
1	1		